What is claimed is:

1. An inter-processor communication apparatus of a mobile communication system comprising:

a data-FIFO for storing a receiving data;

a slave-logic for controlling a writing operation of the data-FIFO and counting the length of the receiving data until an end-tap signal is inputted;

a length-FIFO for storing the data length counted by the slave-logic; and

a CPU for continuously reading the data stored in the data-FIFO as much as the data read from the length-FIFO when an interrupt signal is inputted from the slave-logic.

2. The apparatus of claim 1, wherein the slave-logic counts the length of the receiving data until an end tag signal is inputted.

3. The apparatus of claim 2, wherein the read data length is one frame data length.

4. The apparatus of claim 1, wherein the slave-logic stores the counted data length in the length-FIFO when the end tag signal is inputted and outputs an interrupt signal to the CPU.

5. The apparatus of claim 1, wherein the CPU continuously reads the data stored in the data-FIFO by 1 byte unit as much as the data length stored in the length-FIFO.

6. An inter-processor communication method of a mobile communication system, comprising the steps of:

storing a receiving data in a first region; counting the length of the receiving data stored in the first region;

checking whether an end tag is received;

storing the counted data length in a second region when the end tag is received and outputting an interrupt signal to a CPU ; and

continuously reading the data stored in the first region by the CPU as much as the data length stored in the second region.

7. The method of claim 6, wherein the first and the second regions are FIFO.

8. The method of claim 6, wherein the data length stored in the second region is one frame of data length.

9. The method of claim 6, wherein the CPU continuously reads the data by 1 byte unit.